#### CMPN - Paper / Subject Code: 31901 / Microprocessor

### SEM-I- CHOICE BASED / NOV. 2018

(3 Hours)	80 Mark
N.B. 1) Question No. 1 is compulsory.	W. 45 C. A.
2) Attempt any Three questions out of remaining.	3000
3) Assume suitable data wherever necessary and state them clearly.	
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	20 3 3 6 5 C
O 1 a) Draw and explain memory read masking evals timing discuss in O.S. 2	Tag and Langue
<ul> <li>Q.1 a) Draw and explain memory read machine cycle timing diagram in minimum mode of 8086.</li> <li>b) Write a short note on mixed language programming.</li> </ul>	9 9 19
c) Explain flag register of 80386 microprocessor.	5 1 5 5
d) Give formats of initialization command words(ICW's) of 8259 PIC.	S. 42. 6
Q. 2 a) Explain the maximum mode configuration of 8086 microprocessor.	10
b) Design 8086 based system for following specifications:	
i) 8086 in minimum mode with clock frequency 5MHz.	
ii) 64 KB EPROM using 16KB*8 chips	37575
iii)16 KB RAM using 8KB*8 chips	10
Q. 3 a) Explain the branch prediction logic used in Pentium processor	000
b) Draw and explain the block diagram of 8257 DMA controller.	10 10
	10
Q.4 a) Explain the modes of operation of 80386 microprocessor.	10
b) i) Explain the I/O mode control word format of 8255 PPI.	5
ii) Explain an instruction issue algorithm of Pentium processor.	5
Q.5 a) Differentiate procedure and macro. Write a program to find the factorial of a number using procedu	ire. 10
b) Explain the interrupt structure of 8086 microprocessor	10
	10
Q.6 a) Explain segmentation of 8086 microprocessor. Give its advantages.	10
h) Explain different addressing modes of 8005 missingly of the	10

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# Paper / Subject Code: 31902 / Database Management System TE / CMPN-Y / CHOICE BASED / NOV. 2018

[ Total Marks: 80] (3 Hours) N.B. :(1) Question No.1 is Compulsory. (2) Attempt any 3 questions out of rest. (3) Make suitable assumptions if any. (4) All questions carry equal marks. 1. Define DBA. Discuss role of DBA a) Explain Components of ER Model b) c) Explain ACID Properties of transaction **Explain Database Languages** d) Define Deadlock. Explain Deadlock Detection, Prevention and Recovery 10 2. a) List 5 Significant differences between file processing system and Database 10 b) Management System 10 Explain Overall Architecture of DBMS in detail 3. a) Construct ER diagram and convert into Relational Model for Company 10 b) Which has several Employees working on different types of projects. Several Employees are working on one department. Every Employee has Manager. Several Employees are supervised by one Employee. Explain the concept of Serializability with its types 10 a) 10 b) Explain following Relational Algebra operations with suitable example a) Project of b) Select b) Union d) Cartesian Product Employee(eid, ename, address, city) 10 Works(eid,cid,salary) Company(cid, cname, city) 1) Modify database so that John now lives in Mumbai 2) Find Employees who live in same city as the company for which they work. 3) Give all employees of "AZ Corporation" where there is increase in salary by 15% 4) Find the names of all employees, company name and city of residence such that Employee name begins with 'I' 5) Delete all tuples in works relation for employees of small bank corporation. b) Define Normalization. Discuss 1NF, 2 NF and 3 NF in Detail 10 Write short notes on any two 6. 20 a) Log Based Recovery b) Constraints in SQL

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Specialization and Generalization

Choice Base Paper / Subject Code: 30303 / COMPUTER NETWORKS

**Duration: 3 Hrs** 

Total Marks: 80

IV.D		estion No. 1 is compaisory.	200 A
		empt <b>any three</b> questions, from remaining <b>five</b> questions.	
	3. Fig	ure to the right indicates full marks	
Q.1.	A)	What are guided and unguided transmission media.	5
	B)	Compare various network topologies	5
	C)	Why subnetting is required and how it is done in classful IP addressing.	5
	D)	Explain FTP and the two TCP Connections.	5
Q.2.	A)	Explain TCP/IP model with neat diagram and the functions of each layer.	10
	B)	Explain various Internetworking devices.	10
Q.3.	A)	What is the difference between static and dynamic routing? Explain	10
	<b>D</b> \	Distance Vector Routing with example.	
0.4	B)	What is framing? Explain various framing techniques.	10
Q.4.	A)	What are Berkley socket primitives? Explain in brief.	10
0.5	B)	What is error detection and correction? Explain CRC with example.	10
Q.5.	A)	What is congestion control? Explain open loop and closed loop congestion control.	10
	B)	Explain in brief	10
		a) Telnet and SSH.	
		b) TCP timers	
Q.6.		Write Short Note on ( Any four )	20
		(a) TCP segment header	
	25	(b) Bluetooth Architecture	
	.47 3	(c) Aloha and its types	
Ś	335	(d) SNMP	
25		(e) Design issues for various layers	
- 12 May	11. (3" V	~ 04 X4 X1	

# CMPN/Paper/Subject Code: 31904/Theory of Computer Science Sem-V-CHOICE BASED / NOV 2018

Time: 3 Hours Total Marks: 80 N.B.: (1) Question No.1 is compulsory. (2) Attempt any three questions from the remaining five questions. (3) Make suitable assumptions wherever necessary but justify your assumptions. 1. (a) Explain Chomsky Hierarchy. (b) Differentiate between PDA and NPDA 05 (c)Define Regular Expression and give regular expression for 05 i) Set of all strings over  $\{0, 1\}$  that end with 1 has no substring 0005 (d) Explain Halting Problem. 05 2. (a) Design a Finite State Machine to determine whether ternary number (base 3) 10 (b) Give and Explain formal definition of Pumping Lemma for Regular Language and 10 prove that following language is not regular.  $L = \{ a^m b^{m-1} | m > 0 \}$ 3. (a) Construct PDA accepting the language  $L = \{a^{2n}b^n | n \ge 0\}$ . (b) Consider the following grammar 10 S-iCtSliCtSes a 10  $C \rightarrow b$ For the string 'ibtacibta' find the following: Leftmost derivation Rightmost derivation (iii) Parse tree (iv) Check if above grammar is ambiguous.

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Page 1 of 2

#### Paper / Subject Code: 31904 / Theory of Computer Science

4.	(a) Construct TM to check wellformedness of parenthesis.	10
	(b) Convert following CFG to CNF $S \rightarrow ASA \mid aB$	10
	$A \rightarrow B \mid S$	
	B → b  ∈	
5.	(a) Convert $(0+1)(10)^*(0+1)$ into NFA with $\varepsilon$ -moves and obtain DFA.	10
	(b) Construct Moore and Mealy Machine to convert each occurrence of 100 by 101.	10
6.	Write short note on following (any 4)	10
	<ul><li>(a) Closure properties of Context Frée Language</li><li>(b) Applications of Regular expression and Finite automata</li></ul>	10
	(c) Rice's Theorem	
	(d) Moore and Mealy Machine	
	(e) Universal Turing Machine	

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Paper/Subject Code: 31907/Elective - I Advance Algorithm (DLOC) / NOV-2018 Sem-I-Choice based

(3 Hours)

**Total Marks: 80** 

N.B.: (1) Question No. 1 is compulsory.

(2) Attempt any three questions out of remaining five questions.

- (3) Make suitable assumptions wherever necessary but justify your assumptions.
- Q.1(a) Find the maximum flow for the following network using Ford Fulkerson algorithm: 10 M

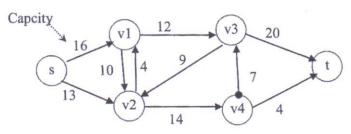


Figure for Q.1 (a)

- Q.1(b) Show TSP is NP Complete and design an approximation algorithm for TSP.
- Q.2(a) What is convex hull? Explain the Graham's scan algorithm 10 M
- Q.2(b) In January, you buy a Ferrari from Lucky Motors, a dealer who offers you the following maintenance contract: Rs.50000 each month other than March, June, September and December (this covers an oil change and general inspection), Rs.1,00,000 every March, June, and September (this covers an oil change, a minor tune-up, and a general inspection), and Rs.2,00,000 every December (this covers an oil change, a major tune-up, and a general inspection).

  Obtain an upper bound on the cost of this maintenance contract as a function of the number of months, using amortized accounting method.
- Q.3 (a) Explain the various methods to find complexity of recursive algorithms.

  Use recursive tree method to find time complexity of the following recursive equation

 $T(n) = 3 T (n/4) + cn^2$ 

Q.3 (b) Create a Red Black Tree for the following elements:

10 M

4, 2, 8, 10, 18, 6, 12, 14

Q.4 (a) What is binomial heap? Draw a binomial heap for the following elements: 10 M

3, 1, 2, 9, 0, 6, 4, 8, 5, 10
After creating binomial heap, delete a node with minimum key and show resultant heap.

- Q.4 (b) Explain Travelling Salesman Problem in details.
- Q.5 (a) Explain with example Maximum Biparite matching.
- Q.5(b) Explain closest pair of points using divide and conquer.
- Q.6(a) What is the hiring problem? Discuss randomized algorithm for the same.
- Q.6(b) Discuss in details line segment properties.

## Paper / Subject Code: 31906 / Elective - I Advance Operating System (DLOC) / NOV-2018.

Marks: 80

### Sem-I-choice based.

**Time: 3 Hours** 

	<ol> <li>Question 1 is compulsory.</li> <li>Attempt any three from the remaining five questions.</li> <li>Assume suitable data where required.</li> </ol>	
1	<ul><li>a.) What do you understand by Hybrid Kernel? Give suitable example.</li><li>b.) Explain Cache Affinity.</li><li>c.) Differentiate between Hard Real Time and Soft Real Time operating system.</li><li>d.) Explain middleware and its role in distributed system.</li></ul>	[5] [5] [5] [5]
2	<ul><li>a.) What is buffer cache? Describe the structure of the buffer header.</li><li>b.) What is U Area? Explain in detail.</li></ul>	[10] [10]
3	<ul><li>a.) Explain Transparency design issues in distributed computing.</li><li>b.) Mach OS ensures location independency during Inter-Process Communication.</li><li>Explain in detail.</li></ul>	[10] [10]
4	<ul><li>a.) Differentiate between Clock Driven and Event Driven scheduler.</li><li>b.) What are the criteria for selecting appropriate frame size in cyclic scheduler? Compute suitable frame size for the following. e stands for executing time, p stands for period and d stands for deadline. All the timing parameters are in miliseconds.</li></ul>	[10] [10]
	e1=1, p1=4 d1=4; e2=1, p2=5 d2=5; e3=1.5, p3=20, d3=20	
5	<ul> <li>a.) Explain Test &amp; Set lock algorithm. Discuss the benefits of Test Test &amp; Set over Test &amp; Set algorithm.</li> </ul>	[10]
	b.)Explain non-uniform memory access (NUMA) architecture? Explain cache coherency in NUMA types multiprocessor.	[10]
6	<ul><li>a.)Discuss various issues of cloud OS.</li><li>b.)Explain Android OS architecture in detail.</li></ul>	[10] [10]