(3 Hours)

[Marks: 80]

N.B.	(1) Question No. 1 is compulsory. 2) Attempt any three questions from remaining questions. 3) All questions carry equal marks.	
бл	(a) (b) (c) (d)	Explain the ARM7 pipeline and justify how hazards are reduced in the pipeline of the ARM7. Explain the concept of register banks in 8051. Explain the power saving modes of the 8051. Explain the function of the barrel shifter in the ARM7 core.	05 05 05 05 05
Q.2	(a) (b)	Write an assembly language program for interfacing an alphanumeric LCD to the 8051. Draw the interfacing diagram. Explain the architecture of the ARM7 core with a neat diagram.	10 10
Q.3	(a)	Write an assembly language program to transfer a block of data in memory using load and store instructions of the ARM7.	10
	(b)	Explain the structure of the Input/Output ports of the 8051 with neat diagrams.	10
Q.4	(a)	Explain the functions of the bits of the CPSR in the ARM7 and differentiate between the CPSR and the SPSR.	10
	(b)	Interface 32K of RAM (using 16K devices) and 32K of ROM (using 16K devices) to the 8051. Show the memory map, clock circuitry and other necessary signals.	10
Q.5	(a)	Write a program (with and without timer) to generate a square wave on pin Pl.2. Highlight the difference in the two methods.	10
	(b)	"ARM-Thumb interworking improves the code density". Justify with a neat example.	10
Q.6	(a)	Write a detailed note on the Interrupt structure of the 8051 and explain the related SFRs.	10
	(b)	Explain the following instructions: (i) MOVC A,@A+DPTR (ii) DJNZ R2,Back (iii) MLA R7,R8,R9,R3 (iv) BLE loop.	20

[Time: Three Hours]

Q.P. Code:13161

[Marks:80]

Please check whether you have got the right question paper. 1. Question.No.1 is compulsory. N.B: 2. Solve any three questions from the remaining. 3. Assume suitable data if necessary. Figures to the right indicate full marks. 05 Compare integrator & differentiator. Define input offset voltage, output offset voltage, input bias current & input offset current for op-amp. 05 05 c) Explain any five specifications of the digital to analog converter (DAC). Describe the basic block diagram of the phase locked Loop (PLL). 05 Derive the expression of inverting & the non-inverting amplifier using op-amp (Av) & design them both for 10 |AV|=10 Derive the output voltage (Vo) expression of op-amp three input averaging circuit. 10 Design 2nd order KRC Low pass filter (LPF) for cut-off frequency fo=10 kHz with quality factor (Q) of 5. 10 Design 1st order high pass filter for a cut-off frequency fo=2 kHz with unity gain. How will you modify the 10 design to achieve low pass filter (LPF) operation? Describe the parallel comparator/ flash type analog to digital convertor (ADC) with a neat diagram. 10 5) Explain the operation of inverting Schmitt Trigger with neat diagram, input & output waveforms with 10 transfer characteristics. \square a) Design Monostable multivibrator using IC 555 to generate a time delay of T=500 ms. Assume + V_{cc} =10V. 10 Design a positive voltage regulator to generate Vo=+5V with Io = 50mA by using IC LM 723. Draw neat 10 diagram of the designed circuit. Write short notes on (any four):a) 3 stage R-C phase shift oscillator using op-amp. Triangular waveform generator using op-amp. Precision Rectifier using op-amp. Log-Antilog amplifier using op-amp. Voltage controlled oscillator (VCO)

Q. P. Code: 18258

Time: 3 Hrs.

Total Marks:80

NOTE

- 1) Question number 1 is compulsory.
- 2) Attempt any three questions from the remaining five questions.
- 3) Assume suitable data wherever necessary,

O1 a How

- a How will you map any point on s-plane to z-plane?
 - b State and prove Duality property of Fourier Transform.
 - c How will you obtain z-transform of the discrete time signal x (nT), from Laplace transform of sampled version of x (t), using Z=eSt.
 - d Find the transfer function of a system having its unit step response given as: s (t) = t u(t) 5 +Sin(t) u(t)
- Q2 a Verify periodicity of the following continuous time signals. If periodic find the fundamental period.
 - $(i)x(t)= 2 \cos(t/4)$
 - (ii) $x(t)=e^{-j2\pi t/7}$
 - b Determine power or energy of the following continuous time signal:
 - (i) $x(t) = 3 \cos(5mt)$
 - (ii) $x(t) = e^{j(2t+\pi/4)}$
 - c Determine whether the following systems are linear/nonlinear, time variant/invariant, causal/noncausal, and stable/unstable.
 - (i) $y(t) = e^{t}$. X(t)
 - (ii) y(t) = cost. x(t)
- Q3 a State the sampling theorem. Discuss the effects of aliasing in frequency spectrum.
 - b Determine the impulse response sequence of the discrete time LTI system defined by

$$Y(n) - 2y(n-1) + y(n-2) = x(n) + 3x(n-3)$$

Q4 a Determine the natural response of the system described by the equation :

$$\frac{d^2y(t)}{dt^2} + 6\frac{dy(t)}{dt} + 5y(t) = \frac{dx(t)}{dt} + 4x(t); \quad y(0) = 1; \frac{dy(t)}{dt} = -2 \text{ at } t = 0$$

TURN OVER

4

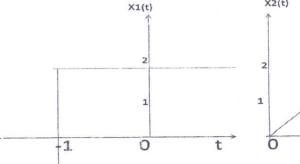
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2

Q. P. Code: 18258

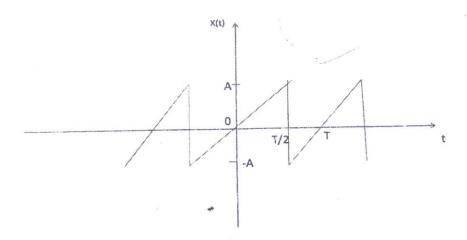
O4 b Perform convolution of the following signals, by graphical method and sketch the resultant signal.

10



Q5 a Determine the trigonometric form of Fourier series for the signal shown in figure:-

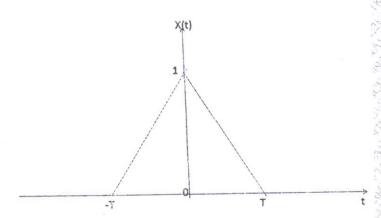
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TURN OVER

3

Determine the Fourier transform of the triangular pulse shown in figure:



Obtain inverse Laplace transform of $X(s) = \frac{4}{(s+1)(s+2)^2}$ for all possible ROC conditions.

10

Determine the Z transform and sketch ROC

10

- 1) $X_1[n] = \left[\frac{1}{3}\right]^n$; $n \ge 0$ 2) $X_2[n] = X_1[n+4]$

Q. P. Code: 13634

Duration:3 hours

Total marks:80

N.S.: (1) Question No.1 is compulsory.

- (2) Solve any four from remaining six questions.
- (3) Figures to the right indicate full marks
- 1. Answer the following questions:

(20)

- (a) Why and how the bandwidth of a signal is spread using spread spectrum.
- (b) Define entropy of an information source and explain its significance.
- (c) Compare and contrast digital communication with analog communication
- (d) Explain the salient features of BFSK.
- (e) Discuss on linearity and cyclic property of linear codes.
- 2(a) Develop MSK waveform (with all intermediate waveforms) for 11000111 for m=5 & m=1 on the graph paper and justify the term "minimum shift keying". (10)
- (b) A (7,4) cyclic code is generated using the polynomial $x^3 + x + 1$
 - i) Generate the systematic cyclic code for the data 1100.
 - ii) Draw the encoder & show how parity bits are generated for the data 1100. (10)
- 3(a) Compare BPSK and QPSK based on following parameters:- bandwidth requirement, noise immunity, transmission rate, efficiency & applications. (10)
- (b) The generator matrix [G] of linear (7,4) block code is as follows:

G= 1111000

1010100

0110010

1100001

- i) Find parity check matrix
- ii) Determine the syndrome for the code word 1101101. State with reasons whether this a valid code word (10)
- 4(a) A three digit message is transmitted over a noisy channel having a probability of error P(e) = (1/5) per digit.
- a Determine Probability of occurrence of errorless message
- b. Determine Probability of message having error in any two digits

Turn Over

Q. P. Code: 13634

2

- c. Determine Probability of message having error in all digits
- d. Plot the all possible probabilities of occurrence of error

(10)

- (b) Distinguish between direct sequence spread spectrum (DSSS) and frequency-hop spread spectrum (FHSS) with respect to principle and applications. (10)
- 5(a) Derive the probability of error of matched filter. Comment on your results. (10)

(b) Consider an alphabet of a discrete memory less source having five different symbols with probabilities as shown below:

Symbol	SI	S2	S3	S4	S5
Probability	0.1	0.2	0.4	0.1	0.2

Construct: (a) Huffman Code for each symbol.

- (b) Determine average codeword length of the above source.
- (c) Comment on your results.

(10)

- 6(a) What is an eye diagram, explain the parameters observed from it with an illustration. (10)
 - . . .
- (b) State Nyquist's Criterion for distortion less transmission. State its significance with duohinary encoding. (10)