**OP Code: 597900** 

(3 Hours)

[ Total Marks :80

10

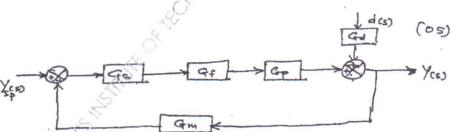
- N.B.: (1) Question no. 1 is compulsory.
  - (2) From Q. No. 2 to Q. No. 6. Solve any three.
  - (3) Assume suitable data wherever necessary.
- Answer any four:-

(a) Explain Gain margin and phase margin.

- (b) Compare Batch process and continuous process.
- (c) Explain Reset windup and its significance.
- (d) Explain Override control with an example.
- (e) Explain smith predictor compensator.
- (a) Explain Z-N method for closed loop system for tuning of controllers.
- (b) In an application of Z-N method a process begins oscillation with 30% proportional Band in an 11.5 min. period. Find nominal PID controller settings.
- (c) Explain Electronic PID controller with neat diagram.

(a) What is setpoint tracking and disturbance Rejection. Explain with an example 5

(b)



Find equation for Y(s) in terms of Ysp(s) and d(s)

- (c) Explain Relay based tuning technique. What are the advantages over Cohen-Coon technique.
- (a) Explain with an example, What is Interaction? Explain decoupling method 10 used in multivariable control.
- (b) What are the objectives of Adaptive control. Explain Self Tuning Regulator. 10

[ TURN OVER

Con.8161-16.

10

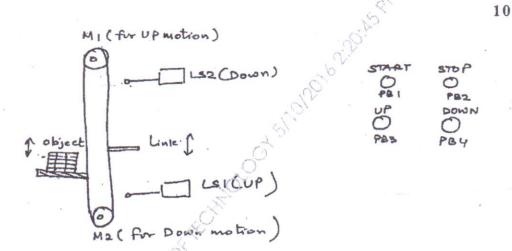
(b) A 5-m diameter cylindrical tank is emptied by a constant outflow of 1.0m³/min. An on off controller is used to open and close a fill value with an open flow of 2.0m³/min for level control, the Neutral zone-is 1m and setpoint is 12m.

10

(i) Calculate cycling period

(ii) Plot level versus time

6. (a)



The Elevator shown in figure above employs a platform to move objects up and down The objective is when 'UP'Button (PB3) is push the platform carries object to the up position upto LSI(Link on other side of object touches LSI), similarly when DOWN Button is pressed (PB4), the platform carries object down til LS2. M1 and M2 are separate motors used for this operation Process start with START(PBI) pressed and stops with STOP(PB2) pressed.

(b) Explain Inverse response. Also give one example.

10

Con.8161-16.

E/INST/Sem-VI (CBSQS)/Power Electronics & Drives/ May-16

Q.P. Code: 598000

[ Total Marks: 80 (3 Hours) N.B.: (1) Question No. 1 is compulsory. Solve any three from the remaining. (a) Define delay time, rise time & spread time referred to SCR. (b) Explain the effect of source inductance on fully controlled bridge rectifier. (c) How are inverters classified? (d) Explain basic block diagram of SMPS. (e) Explain how choice of a drive can be made. Design AC power control circuit using TRIAC & DIAC for following 20 requirements. Input voltage =250V ac, 50 Hz, 1 phase Output voltage variation =150 to 200 V Load resistance =  $300\Omega$ (a) Explain Buck-Boost converter. Derive expression for output voltage. 10 (b) Explain variable frequency control drive for AC motors. 10 (a) What are dv/dt& di/dt rating of \$CR? What happens if these ratings are 10 exceeded? Explain. (b) Explain applications of power electronics in electric heating. 10 (a) Compare MOSFET, IGBT & SCR. 10 (b) Explain PWM full bridge inverter. 10 (a) Explain different triggering methods of SCR. Also write their advantages 10 & disadvantages. (b) Write short note on. 10 1) Series inverter

2) Step up & step down chopper

## sem-III-(CBSas) INST/ Digital Signal Processing Marts

QP Code: 598101

(3 Hours) [ Total Marks: 80 N.B.: 1. Question No. 1 is compulsory. 2. Attempt any three questions out of remaining five questions. 3. Assume suitable data if necessary. Answer the following (Any four):-(a)  $H(z) = \frac{5z^2 - 12z}{z^2 - 6z + 8}$  show that  $h(n) = 2^n + 4^{n+1}$  and find first 5 vaccies. (b) What are the advantage of DSP & define sampling theorem. (c) Determine IDFT of  $x(k) = \{3, 2+j, 1, 2-j\}$  by using DIF FFT algorithm. (d) Convert the anlang filter with system function  $H(s) = \frac{(s+0.1)}{(s+0.1)^2+16}$  into 10 a digital IIR filter using Bilinear transformation. The resanant frequency of  $\omega r = \pi/2$ . (e) Write a short note on Decimation by a integer factor. 10 (a) If  $x(n) = \{2, 3, 4, 5\}$  find (i) DFT of x(k) (ii) using result obtained in one 10 not otherwise find the DFT of following sequences.  $x_1(n) = \{5, 2, 3, 4\}, x_2(n) = \{3, 4, 5, 2\}$  $[4, 5, 2, 3] x_3 = [2, 5, 4, 3]$ (b) Perform Linear convolution using DIT FFT algorithm. 10  $x(n) = \{1, 2, 3\}$ h(n) = [1, 2](a) Determine the output of a Lirear FIR & whose impuse response 10  $h(n) = \{2, 2, 1\}$  $x(n) = \{3, 0, -2, 0, 2, 1, 0, -2, -1, 0\}$  using over lap save method. 10 (b) Derive & draw the FFT for  $N = 6 = 2 \times 3$  using DIT FFT algorithm. (a) Determine the frequency response plot magnitude & phase response for 10 the frequency  $\omega = 0$ ,  $\pi/4$ ,  $\pi/2$ ,  $3\pi/4$ , &  $\pi$ . y(n) = x(n) + 0.9 x(n-2) - 0.4 y(n-2)

[TURN OVER]

(b) Realize the system by using, direct form - I cascade & parallel Realization. y(n) = -0.1y(n-1) + 0.2y(n-2) + 3x(n) + 3.6 x(n-1) + 0.6 x(n-2)
 5. (a) Design IIR butter worth filter to satisfy following condition. 0.8 < | H (e<sup>jω</sup>) | ≤ 1 for 0 ≤ ω ≤ 0.2π

 $\begin{array}{ll} 0.8 < |\,H\,(e^{j\omega})\,| \leq 1 & \text{for } 0 \leq \omega \leq 0.2\pi \\ |\,H(e^{j\omega})\,| \leq 0.2 & \text{for } 0.6\pi \leq \omega \leq \pi \\ \text{using Bilirear transfromation method Assume T} = 1\text{sec.} \end{array}$ 

- (b) A Linear phase FIR filter has derived  $\begin{array}{ll} \text{Ha}(e^{j\omega}) &= 0 & \text{for } -\pi/4 \leq \omega \leq \pi \; /4 \\ &= e^{-j2\omega} & \text{for } \pi/4 \; | \leq |w| < \pi \\ \text{Design the filter using Hanning window Assume M} = 5 \text{ and also draw Linear phase Realization.} \end{array}$
- 6. (a) Explain the Architecture of Tex as 320 DSP processor.
  - (b) Write a short note on Interpalation.
  - (c) Difference between IIR & FIR filter.

(3 Hours)

[Total Marks:80

0. 0		~	
		н.	
8.74	-	₽.	

(1)Question	No.	1	is	compulsory.
-------------	-----	---	----	-------------

- (2) Attempt any three questions out of remaining five questions.
- (3) Assume suitable data if necessary

1.	Answer following.	20				
a.	Explain characteristics of embedded systems					
b.	Interface one 7-segment LED display to PIC18F4520 and					
	write instructions/program to display '8' on it.					
C.	Explain following instructions with example					
	i. DECFST ii. BTFSS					
d.	With suitable example explain priority inversion					
e.	Explain PORTB change interrupt of PIC18F.					
_	- 11 CDYC10F4600	10				
2. a.	Draw and explain programming model of PIC18F4520.	10				
.b.	Write a program to convert HEX number to decimal number	10				
	using PIC18F assembly language or C programming.					
	Explain program logic in detail with example.					
3. a.	Interface 15x2 LCD module to PIC18. Write a program to	10				
	display "INSTRU" on first line and "ENGINEERING" on					
	second line					
b.	Write a program for PIC18 to transfer the string 'INSTRU'					
	serially at 9600 baud continuously. Assume XTAL=8Mhz					
4. a.	Interface 8 keys and one 7-segment display to PIC18. Write a	10				
1 Pin	program to display key number which is pressed. If no key is					
1	pressed display '0'.					
· b.	What is task? Explain various task scheduling algorithms.	10				

[Turn Over

5. a.	Explain onchip ADC module of PIC18F4520. Write a	10
	program to to convert analog signal at AN0 and out it on	
	PORTB and PORTD.	
· b.	Explain block diagram of temperature measurement / control	10
	system using PIC18F4520. Explain program flowchart.	Ġ,
6.	Write short note on any two	20
a.	Development tools for PIC18F4520. Explain role of each	
	tool in PIC18F based project.	
b.	On- chip ECCP module of PIC18F4520. Explain PWM	
	mode in detail.	

## PIC 18f4520 SFRS

c. Memory organization of PIC18F.

				133				
Reg. Name	Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Blt1	BitO
INTCON	GIE/GIH	PEIE/GIEL	TMROIE	INTOLE	RBIE	TMROIF	INTOIF	RBIF
INTCON2	RBPU	INTEDGO	INTEDR1	INTEDG2		TMROIP		RBIP
INTCON3	INT2IP	INT1IP	, (c)	INT2IE	INT1IE	•	INT2IF	INT1IF
T1CON	RD16	T1RUN	T1CKPS1	T1CKPS0	T10SCEN	T1SYNC	TMR1CS	TMR10N
T2CON	5-046	T2OUTPS3	T2QUTPS2	T2OUTPS1	T2OUTPS0	TMR2ON	T2CKPS1	T2CKPS0
ADCON0	-		CHS3	CHS2	CHS1	CHS0	GO/DONE	ADON
ADCON1	-		VCFG1	VCFGO	PCFG3	PCFG2	PCFG1	PCFG0
ADCON2	ADFM	- 4	ACQT2	NDB	ACQTO	ADCS2	DACS1	ADCS0
TXSTA	CSRC	TX9	TXEN	SYNC	SENDB	BRGH	TRMT	TX9D
RCSTA	SPEN	RX9	SREN	CREN	ADDEN	FERR	OERR	TX9D
IPR1	PSPIP	ADIP	RCIP	TXIP	SSPIP	CCP1IP	TMR2IP	TMR1IP
IPR2	OSCFIP	CMIP	-	EEIF	BCLIP .	HLVDIP	TMR3IP	CCP2IP
PIE1	PSPIE	ADIE	RCIE	TXIE	SSPIE	CCP1IE	TMR2IE	TMR1IE
PIE2	OSCFIE	CMIE		EEIE	BCLIE	HLVDIE	TMR3IE	CCP21E
PIR1	PSPIF	ADIF	RCIF	TXIF	SSPIF	CCP1IF	TMR2IF	TMR1IF
PIR2	OSCFIF	CMIF		EEIF	BCLIF	HLVDIF	TMR3IF	CCP2IF

Elsem-VI (CBSes) INST/ Industrial Data Communication) Q. P. Code: 598301 [Total Marks: 80 (3 Hours) Note: Question 1 is compulsory Solve any three out of remaining five questions Figures on right indicate full marks allotted to main question Q1 Answer the following a. What are different types of network topologies? b. Explain Burst mode of communication of HART. c. Which layer is responsible for following-Physical encoding and decoding Providing end to end delivery of packets Providing end to end delivery of segments 111. Error detection and correction IV. V. Routing d. What is FFB Foundation fieldbus? List the features 20Mks Q2 Answer the following a) Explain The OSI model b) Draw the TCP/IP model and Explain LAN Architecture including IEEE standards 20Mks Q3 Answer the following a) Compare H1 and HSE segment of Foundation Field bus b) What is CAN Bus? Explain? 20Mks Q4 Answer the following a) Explain Three components (Blocks) of Foundation fieldBus b) What is MODBUS and MODBUS PLUS 20Mks Q5 Answer the following a) What is Profibus? Explain Profibus PA, Profibus DP and Profibus FMS b) Explain HART protocol and give its importance in predictive maintenance 20Mks Q6 Write short notes on (Any four) RS232 i.

**GPRS** 

VSAT

Wireless LAN

11.

## Sem-YI (CBSGS) / INST/ Analytical Instrumentation/May-16, QP CODE: 598402

(3 Hours) [Total Mark	ks : 80
N.B. (1), Question No.1 is Compulsory.  (2) Attempt any three questions remaining four Questions.  (3) Figures to the right indicates full marks.	14
<ul> <li>Q.1.Attempt any five:-</li> <li>a) Draw the general block diagram of analytical instruments and explain.</li> <li>b) Describe the radiation sources used in IR spectrophotometers.</li> <li>c) Define chemical shift and explain its significance in NMR spectrometry.</li> <li>d) State Beers and Lambert,s Law.</li> <li>e) Give the list of components of mass spectrometers. Explain in brief the principle of mass spectrometry.</li> <li>f) What are the units of radioactivity. Explain half life period.</li> </ul>	(20)
Q.2 a) With a neat block diagram explain an atomic absorption spectrophotometer. b) When does nuclear magnetic resonance occur. Explain the working of NMR	(10)
spectrometer	(10)
Q.3a) Explain with a schematic diagram the operation of a double beam UV Spectron	neter(10)
b) Explain Time of flight mass spectrometer with neat diagram.	(10)
Q.4 a)Explain the basic components of instrumentation for X-ray spectroscopy with a diagram b)Give classification of chromatograph, List the parts of GC. Draw and explain	(10)
the working of GC.	(10)
Q.5 a)Explain the principle and working of Scintillation counter b) With neat diagram explain paramagnetic oxygen analyzer	(10) (10)
Q.6 Write short notes on(any two):- a) ESR b) Infrared gas analyzer	(20)

c) GC-MS