Rev. 2012 Q.P. Code: 24637

Duration: 03 Hrs.

Total Marks: 80

Note:

- 1) Q. No 1 is compulsory.
- 2) Attempt any THREE questions from Q No 2 to Q No 6.
- 3) Assume suitable Data wherever necessary.

(1) Attempt Any Four

20M

- a) Explain Selective Control Scheme.
- b) Explain in brief PV, SV, CV, MV with reference to Process control.
- c) Derive equation for Dead Time process. Give Pade' Approximation.
- b) Explain need of Process control.
- e) Explain Master Recipe, Control Recipe.

QLNo 2)

- 2) Explain with a neat sketch working of Hydraulic PD controller. 10M
- Explain working of Single Speed Floating Control Mode. 10M

QL No 3)

- Explain Different Physical Ladder Diagram Elements. 10M
- b) What is the objective of Adaptive Control System. Explain Self 10M Tuning Regulator Method of Adaptive Control.

(No 4)

- Explain in Detail Partial Decoupling and Full Decoupling 10M method with respect to MIMO systems.
- Explain Feedback & FeedForward control system for Stirred 10M Tank Heater system.

10M

10M

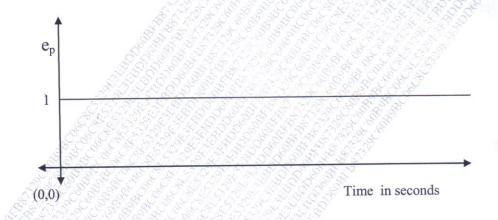
Q No 5)

- a) Explain One quarter Decay ratio and its significance. Explain Ziegler- Nichol's and Cohen Coon Open Loop method for tuning.
- b)Prove Proportional control introduces offset in closed loop with first order process. (Assume $G_f = G_m = 1$).

Q. No 6

a) Explain Dynamic behavior of First and Second order system.

b) Plot response of P, PI, PD and PID for following error plot. 10M Kp=1%/%, Ki=1%/(%-seconds) & Kd=1%/(%/seconds).



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Q.P.Code: 40101

	[Total Mark	s: 80)
: .a.v	1. Question I is compulsory. 2. Soive any three from remaining. 3. Assume suitable data if required and justify it.	
Q.1)	Attempt any four	[20]
a. b. c. d.	Explain Punch through IGBT Explain class C commutation circuit Write short note on reduction of harmonic distortion. List out advantage and disadvantages of series inverter and parallel inverter. Explain Type B chopper	
Q.2)	Design a converter to give output voltage 180 V at 1A load current. The input is 230 V 50 Hz ac supply. Use UJT 2N2646.V _{BBmax} = 35 V for Vbb=16 V. C=0 1 μ F , η_{ton} =0.56 , η_{ton} =0.75. η_{type} =0.631v=4 μ A, 1p=25 μ A Consider temperature compensation.	[20]
13 21		
Q.3) a. b.	Explain variable AC induction rooter drive. Explain basic penciple of Dielectric heating. List its advantages and applications.	[10]
(0.4)		
a. b.	Explain fan regulator usong diac-triae scheme. What are the different PWM techniques? Explain with neat waveforms.	[10]
Q.5)		
a.		[10]
ъ.	operation of Buck-Boost regulator Explain symmetric semiconverter.	[10]
(0.6)		
a. b.	Draw the next diagram and waveforms and explain Jones Chopper Explain circuit diagram of full bridge inverter with free-wheeling diode.	[10]

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Q.P.Code: 40101

	[3 Hours] [Total Mark	s: 80]
V.B. :	1. Question 1 is compulsory. 2. Soive any three from remaining 3. Assume suitable data if required and justify it.	
(1.1)	Attempt any four	[20]
a. b. c. d.	Explain Punch through IGBT Explain class C commutation circuit Write short note on reduction of harmonic distortion. List out advantage and disadvantages of series inverter and parallel inverter. Explain Type B chopper	
2.2)	Design a converter to give output voltage 180 V at 1A load current. The input is 230 V 50 Hz ac supply. Use UJT 2N2646.VBRmax= 35 V for Vbb=16 V, C=0.1 μ F , η_{min} =0.56 , η_{min} =0.75. η_{type} =0.63h=4m.A, 1p=25 μ A Consider temperature compensation.	[20]
2 27		
Q.3) a. b	Explain variable AC induction motor drive. Explain basic penciple of Dielectric heating. List its advantages and applications.	[10]
0.4)		
а. Ъ.	Explain fan regulator using diac-triac scheme. What are the different PWM techniques? Explain with neat waveforms.	[10] [10]
3 (4)		
Q.5) a.	With the help of a neat diagram and associated waveforms discuss the operation of Buck-Boost regulator	[10]
Ь.	Explain symmetric semiconverter.	[10]
Q.6)		
a. b.	Draw the neat diagram and waveforms and explain Jones Chopper Explain circuit diagram of full bridge inverter with free-wheeling diode.	[10] [10]

QP Code: 13490

(3 Hours)

[Total Marks: 80

- M.B.: (1) Question No. 1 is compulsory.
 - (2) Attemt any three questions from the remaining five questions.
 - (3) Assume suitable data if necessary.
 - 1 Answer the following any four :-

20

- (a) Find the Z-transform of
 - (i) $x(n) = a^n u(n)$

(ii)
$$x(n) = -u(-n-1)$$

also specify the ROC.

(b) For analog transfer function

$$Ha(s) = \frac{3}{(s+1)(s+2)}$$
 Determine $H(z)$ using

impulse invarient method assume T = 2sec

- (c) If $x(k) = \{8, -2-2j, 0, -2+2j\}$ find the IDFT.
- (d) Difference b/w Linear and circular convolution.
- (e) Define sampling theorm. What are the advantages DSP.
- Find the DFT of $x(n) = [1 \ 1 \ 2 \ 3]$ using the above result and not otherwise find the DFT

$$x_1(n) = \{1, 0, 1, 0, 2, 0, 3, 0\}$$

$$x_2(n) = \{1, 0, 0, 1, 0, 0, 2, 0, 0, 3, 0, 0\}$$

$$x_3(n) = [1, 1, 2, 3, 1, 1, 2, 3]$$

x(n) = {2, 2, 2, 2, 1, 1, 1, 1} compute 8 point DFT with Radix-2 DIT FFT algorithm.

10

- The input sequence x(n) = [1, 1, 2, -1, 2, -3, -1, 1, 2, 1, -3, -1] having the impulse response 10 of FIR filter h(n) = {2, 3, 4} using overlap save method find o/p response.
 - Determine the frequency response of the system y(n) = 0.9y(n-2) + 0.2x(n). Find magnitude 10 and phase response of it.
- Realize the systme using D.F. II, cascade and parallel realization 5

$$y(n) = x(n) + \frac{1}{4}x(n-1) + \frac{1}{6}y(n-1) + \frac{1}{6}y(n-2)$$

- $= e^{-j3w}$ [0.5+2.2cos3w]+[1.2cos2w+0.4cosw] obtain order and impulse response
- mite a short note on comb filter and notch filter.

10

TURN OVER

5. (a) Design a digital Butterworth fiture that satisfy the following constraints using Bilinear transformation.

Assume T = 1 sec

$$\begin{array}{ccc} 0.9 \, \leq \, H \left(e^{j \, W} \right) \! \! \leq \, 1 & \quad 0 \leq w \leq \, 0.25 \, \pi \\ & \quad H \left(e^{j \, W} \right) \! \! \leq \, 0.2 & \quad 0.6 \, \pi \leq w \leq \, \pi \end{array}$$

(b) A linear phase FIR filure has desired response

$$H_d\left(e^{jw}\right) = 0 \text{ for } -3\frac{\pi}{4} \le w \le \frac{3\pi}{4}$$

= $e^{-j2w} \text{ for } 3\frac{\pi}{4} < |w| \le \pi$

Design the filter using Hamming window also draw linear phase realization.

- 6. (a) Write a short note on 'Decimation by integer factors'.
 - (b) Explain the application of DSP processor (Texas-320).
 - (c) If the order of filter N = 2 find the transfer function at IIR filter (poles of IIR filter).

	T	(Inst. Engg) Per. 2012. (Inst. Engg) Application of MicroController II (3 Hours) [Total Marks: 8	
N.	B.:		
	1) Q	uestion no.1 is compulsory.	
	2) A	ttempt any three questions out of remaining five questions.	
	3) A	ssume suitable data if necessary.	
1		Solve following	20
	a	Explain the structure of STATUS (Flag) register of PIC18F.	
	b c d	What are the registers associated with PIC18F452 ports? Describe the role of each in brief? Explain the following instructions of PIC18F with example a. SETF b. BTFSS c. RRNCF d. CPFSEQ Define embedded system? List any four characteristics of it.	
	e	Write a program to multiply two 8-bit numbers	
2	a	Draw a diagram to Interface 4 x 4 matrix key board to PIC 18F and explain logic to identify which key is pressed.	10
0	b	Draw an interfacing diagram to connect 16 x 2 LCD Module to PIC18. Write a Program to display 'WELCOME' on it.	10
3	a	Explain the working of CCP module of PIC18F?	10
2 1 X X X X X X X X X X X X X X X X X X	b	Write a program to generate a square wave using timer, on RC0 pin, assume clock frequency is 16MHz.	10
4	a	Write a program to convert ASCII number to BCD number with flowchart.	10
000	b	What is a task? Explain various task scheduling algorithms.	10

3490

10

TURN OVER

Explain the process of transmitting a character with serial 10 communication module of PIC18F. Write a program for the same.

What are different design metrics of embedded system? Explain. 10

6 a Explain the working of I2C communication bus.

10

b Explain the ADC module of PIC18F.

10

PIC18F4520 SFRS

Reg. Name	Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0
INTCON	GIE/GIH	PEIE/GIEL	TMR0IE	INT0IE	PBIE	TMR0IF	INTOIF	RBIF
INTCON2	RBPU	INTEDG0	INTEDG1	INTEDG2		TMR0IP	70 30 3°	RBIP
INTCON3	INT2IP	INT1IP		INT2IE	INTHE		INT2IF	INTIIF
T0CON	TMR00N	T08BIT	TOCS	T0SE	PSA	T0PS2	T0PS1	T0PS0
TICON	RD16	T1RUN	T1CKPS1	T1CKPS0	T10SCEN	T1SYNC	TMR1CS	TMR10N
T2CON	_	T2OUTPS3	T2OUTPS2	T2OUTP\$1	T2OUTPS0	TMR2ON	T2CKPS1	T2CKPS0
ADCON0		-	CHS3	CHS2	CHS1	CHS0	GO/DONE	ADON
ADCON1		88	VCFG1	VCFG0	PCFG3	PCFG2	PCFG1	PCFG0
ADCON2	ADFM	770,00	ACQT2	ACQT1	ACQT0	ADCS2	ADCS1	ADCS0
TXSTA	CSRC	TX9	TXEN	SYNC	SENDB	BRGH	TRMT	TX9D
RCSTA	SPEN	RX9	SREN	CREN	ADDEN	FERR	OERR	RX9D
IPR1	PSPIF	ADIF	RCIF	TXIF	SSPIF	CCP1IF	TMR2IF	TMR1IF
IPR2	OSCFIF	CMIF	2000	EEIF	BCLIF	HLVDIF	TMR3IF	CCP2IF
PIE1	PSPIE	ADIE	RCIE	TXIE	SSPIE	CCP1IE	TMR2IE	TMR1IE
PIE2	OSCFIE	CMIE		EEIE	BCLIE	HLVDIE	TMR3IE	CCP2IE
PIRI	PSPIP(1)	ADIP	RCIP	TXIP	SSPIP	CCP1IP	TMR2IP	TMR1IP
PIR2	OSCFIP	CMIP	37.27.50	EEIP	BCLIP	HLVDIP	TMR3IP	CCP2IP

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Q.P. Code: 36590

	[Time: Three Hours]		[Marks:80]
		\$ 00°C	
Wote:			
		4	
1. Questio	n No.1 is compulsory		÷ Y.
2. Solve a	ny THREE questions out of remaining FIVE questions.	, F.	
3. Figure t	to the right indicate full marks.	**	
	병이 잃었다면 그리고 그 그렇게 그렇게 되는 것이다.		
L Answer	the following:-		(20)
a) Exp	lain Menchester coding.		
	npare H1 and HSE segment of foundation Field bus		
	at are the different types of network topologies.		
	ich standard is used for:	a: p	
	ireless LAN ii GPRS iii Bluefooth iv 2 at are the criteria to be considered to establish a good net	ZigBee	
c) wii	at are the effecta to be considered to establish a good her	WOIK!	
1 Answer	he following:-		
a) Draw	and explain the OSI reference model in detail.		(10)
	ain the following network components and state in which	layer of OSI	
mode	el each of them work:		
	i) Bridge		
	ii) Hub iii) Router		
	iv) Gateway		
	v) Switch		
3 Answer	the following:-		
a) Wh	at is MODBUS and MODBUS Plus?		(10)
	cribe PROFI BUS-DP in detail.		(10)
			3
4 Answer	the following:-		
a) Die	and the OPC exchitication with witching discussion		(10)
	cuss the OPC architecture with suitable diagrams.	zardous area	(10) (10)
		zaidous alea.	(10)
= Answer	the following:-		
a) Expl	ain the architecture of HART.		(10)
b) Wha	t is satellite communication? Define uplink and downlink	k frequency?	(10)
E 100 A			(00)
tion.	hort notes on		(20)
10.4	ta Highway Plus		
	N protocol		
	232,RS422		

Q.P.Code: 50717

03 Hours

Total Marks: 80

Note:	1) Question No. 1 is compulsory.	3 43
	2) Answer any three questions from the remaining five questions	
	3) Illustrate answers with sketches wherever required.	X X
	4) Figures to the right indicate marks.	20 B
QL.A	nswer the following:	20
	 a) Draw the general block diagram of analytical Instrument and explain. b) Define chemical shift and explain its significance in NMR Spectrometry. c) Explain interference filters used in the absorption instruments. d) What are the units of Radioactivity? Explain half-life period. 	
72	a) What is meant by Raman Effect? List the characteristic properties of Raman lines.	Ű'
	Draw and explain the constructional detail of Raman spectrometer.	10
	b) Explain differences between atomic absorption and atomic emission spectroscopy.	10
100	a) Explain Magnetic deflection type Mass spectrometer with neat diagram.	10
	b) When does magnetic resonance occur? Explain working of NMR Spectrometer.	10
[K	a) Give classification of chromatograph. List the parts of GC. Draw and explain the	
	working of GC:	10
	b) With a neat schematic diagram, explain Scintillation counter.	10
	3) Explain the basic components of Instrumentation for x-ray spectroscopy with	
	a neat diagram.	10
	b) With a neat diagram, explain Double beam spectrometer.	10
	Write short note on:	20
	a) Photomultiplier tube	
	b) GC-MS	
	D Oxygen analyzer	
	# GM counter	