

ABOUT US

Vivekanand Education Society's Institute of Technology (VESIT), established in 1984, is one of the most premium institutions for engineering affiliated to the University of Mumbai. VESIT has been practicing innovative teaching practices to attain its mission of being committed to excellence in technological education, research and training, use of contemporary participant-centric pedagogies and teaching methods and establishing a presence in emerging segments of technological education.

The department of Computer Engineering is committed towards imparting quality education and developing future technocrats in the stream of computers. It is focussed towards its mission of facilitating student's progress by providing a strong foundation in fundamental concepts, critical thinking skills as well as inculcating core values of professionalism and ethics.

VISION

- To establish a center of excellence in computing by empowering students with advanced technological skills for fostering innovation, research, and societal impact.

MISSION

- To cultivate a teaching learning environment that unites theory with computing skills, empowering students to adapt to evolving technologies.
- To strengthen core computing knowledge by fostering research, innovation, and entrepreneurship through multi-disciplinary learning.
- To prepare competent professionals with industry-ready skills, encouraging social awareness, sustainable development, and a commitment to lifelong learning.



VIVEKANAND EDUCATION SOCIETY'S INSTITUTE OF TECHNOLOGY

(AUTONOMOUS INSTITUTE AFFILIATED TO UNIVERSITY OF MUMBAI, APPROVED BY AICTE & RECOGNIZED BY GOVT. OF MAHARASHTRA)

CHIEF PATRONS

- Shri. B. L. Boolani, Managing Trustee, VESIT
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- Dr. (Mrs.) J. M. Nair, Principal, VESIT
- Dr. (Mrs.) M. Vijayalakshmi, Vice Principal, VESIT

CONVENOR

- Dr. Nupur Giri, Head of the Department, CMPN, VESIT

ADVISORY COMMITTEE

- Dr. Sharmila Sengupta, Professor, CMPN, VESIT
- Dr. Sujata Khedkar, Associate Professor, CMPN, VESIT
- Dr. M. D. Patil, Associate Professor, CMPN, VESIT
- Dr. Dashrath Mane, Associate Professor, CMPN, VESIT
- Dr. Prashant Kanade, Assistant Professor, CMPN, VESIT
- Dr. Rohini Temkar, Assistant Professor, CMPN, VESIT



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DEPARTMENT OF COMPUTER ENGINEERING IN ASSOCIATION WITH VESIT-IQAC AND IIC PRESENTS

ONE-WEEK ISTE-APPROVED SHORT TERM INDUSTRIAL TRAINING PROGRAMME ON

STTP No. MAH103

Data Engineering: Powering Data-Driven Innovation

12th Jan 2026 - 17th Jan 2026

IN OFFLINE MODE

COORDINATORS

Dr. (Mrs.) Gresha Bhatia
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VENUE

HASHU ADVANI MEMORIAL COMPLEX, COLLECTOR COLONY, CHEMBUR, MUMBAI, MAHARASHTRA 400074



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VISIT US

<https://vesit.ves.ac.in>



ABOUT PROGRAMME

One-Week ISTE-Approved Short Term Training Programme on Industrial Training in “Data Engineering: Powering Data-Driven Innovation” aims to offer participants an in-depth understanding of modern data engineering concepts, tools, and architectures that power data-driven decision making in today’s digital ecosystem. Through expert sessions, live demonstrations, and hands-on exercises, participants will gain exposure to industry-standard technologies used in data pipelines, data lakes, cloud platforms, and AI-enabled analytics. It emphasizes a practical, industry-aligned approach, helping faculty members, researchers, and professionals enhance their expertise in data engineering workflows from data ingestion and transformation to storage, modeling, and visualization.

PROGRAMME OBJECTIVES

- Familiarize participants with the fundamentals and advancements in Data Engineering — covering data collection, integration, transformation, and management processes.
- Bridge the gap between academic concepts and industry practices through real-world case studies and demonstrations of end-to-end data pipelines.
- Introduce modern data engineering tools and frameworks such as Apache Spark, Kafka, Airflow, Snowflake, and Databricks to build scalable data solutions.
- Highlight the role of cloud platforms and AI integration in enabling intelligent, automated, and efficient data-driven systems.
- Develop practical skills for designing, deploying, and optimizing data pipelines suitable for analytics, machine learning, and business intelligence applications.
- Encourage collaboration between academia and industry, fostering innovation and research opportunities in emerging areas of data engineering.

PROGRAM OUTCOMES

- Apply the core concepts and principles of data engineering in real-world contexts.
- Use modern tools and technologies such as Apache Spark, Kafka, Airflow, Snowflake, and Databricks for data processing.
- Design and implement efficient, scalable data pipelines for structured and unstructured data.
- Integrate AI and machine learning techniques into data engineering workflows for intelligent analytics.
- Stay updated with emerging trends and engage in continuous learning in data-driven technologies.
- Collaborate effectively with peers and industry experts to develop innovative data solutions.

ORGANISING COMMITTEE

- Mrs. Mannat Doulhani,
Assistant Professor,
9665414514
- Mrs. Pallavi Saindane,
Assistant Professor,
9987796823

RESOURCE PERSONS

- Dr. Nupur Giri, HoD, Dept. of Computer Engineering, VESIT
- Dr. Gresha Bhatia, Professor, MCA, VESIT
- Mr. Yati Gharat, CTO, Litmus Information Systems LLP
- Mr. Rajaram Chitrigemath, Field Application Engineer (FAE), NVIDIA
- Mr. Yogesh Nagdev, Desktop Channel Marketing, NVIDIA
- Mr. Vimal Chaubey, Principal Engineer, R&D Tech, GSK
- Mrs. Anindya Das, CTO, Neysa
- Mrs. Sohinee Chattopadhyay, Head - Partnerships & Growth - Edu/Research, Neysa
- Dr. Sujata Khedkar, Associate Professor, CMPN, VESIT
- Dr. Priya R. L., Assistant Professor, CMPN, VESIT
- Mrs. Abha Tewari, Assistant Professor, CMPN, VESIT

PARTICIPANT GUIDELINES

Who can register: Faculty /PG Student /Research Scholars /Industry Participants

- Participants need to upload transaction receipt in the registration form
- STTP Registration FEES : Rs. 2000/- (Scan QR Code for Payment)
- [STTP Registration Link](#) (Scan QR for Registration)

