



Vivekanand Education Society's
Institute of Technology

(Autonomous)

Approved by AICTE, Affiliated to University of Mumbai

Department of
Electronics and Computer Science

Syllabus Scheme

SEM - VII
A.Y. 2025-26

Semester VII Marking Scheme							
Course Type	Course Name	TH	MT	CA	TW	PR/OR	Total
ECC 701	VLSI Design	60	20	20	-	-	100
ECC 702	Internet of Things	60	20	20	-	-	100
ECC DO701	Department Level Optional Course – III	60	20	20	-	-	100
ECC DO702	Department Level Optional Course – IV	60	20	20	-	-	100
ECC IO701	Institute Level Optional Course- I	60	20	20	-	-	100
ECL701	VLSI Design Lab	-	-	-	25	25	50
ECL702	Internet of Things Lab	-	-	-	25	25	50
ECL703	Department Level Optional Course - III Lab	-	-	-	25	25	50
ECP701	Major Project – I	-	-	-	50	-	50
Total Marks							700



Vivekanand Education Society's Institute of Technology

(Autonomous)

Approved by AICTE, Affiliated to University of Mumbai

Department Level Optional Courses

Department Level Optional Course-III (DO701)	Department Level Optional Course -IV (DO702)
1. Deep Learning	1. Cloud Computing
2. Image Processing	2. Mobile Communication
3. Big Data Analytics	3. Cyber Security
4. Advanced Database Management Systems	4. Blockchain Technology

Institute Level Optional Courses: I

ILO7011	Product Life Cycle Management
ILO7012	Reliability Engineering
ILO7013	Management Information System
ILO7014	Design of Experiments
ILO7015	Operations Research
ILO7016	Cyber Security and Laws
ILO7017	Disaster Management and Mitigation Measures
ILO7018	Energy Audit and Management
ILO7019	Development Engineering