

REFLECTIONS



MESSAGE FROM HOD'S DESK

Dear Students,

'Quality is a journey and not a final destination.'

Accreditation is a process which certifies the competency of a particular program based on the outcomes generated by it. This quality assurance process by the National Board of Accreditation will be one more step towards crossing the benchmark set for quality engineering education, internationally. Accreditation benefits all stakeholders, especially, the students. It will be a matter of great pride for a student to be a part of an accredited program and it will be a great value addition from the industry's perspective.

So let us all join hands in this endeavor and continue the journey towards better quality.

Wishing you all a very Happy New Year 2015

Mrs. Shoba Krishnan

HOD, EXTC

VISION

Towards developing a center of excellence in the field of Electronics and Telecommunication and to nurture students to become technocrats with a humane outlook.

MISSION

- ❖ *To empower students to meet the growing challenges of industry.*
- ❖ *To promote a cutting-edge research to benefit the society locally and globally.*
- ❖ *To develop young engineers with human and social intellectual qualities required for practical responsible engineers.*

PROGRAM EDUCATIONAL OBJECTIVES (PEO)

1. *To prepare students to aptly apply their acquired knowledge of engineering fundamentals and core concepts in Electronics and Telecommunications.*
2. *To contribute to the needs of society in solving real life technical challenges using Electronics and Telecommunication engineering principle tools and practices.*
3. *To enable students to be successful technocrats with effective communication skills and be socially conscious with strong ethical and balanced outlook.*
4. *To create and provide a conducive environment suitable for lifelong learning, successful entrepreneurship, multidisciplinary engineering challenges and to tackle the contemporary issues.*

PROGRAM OUTCOMES (PO)

- a. **Engineering Knowledge:** *An ability to apply fundamental concepts of Maths, science & engineering to solve Electronics and Telecommunication problems.*
- b. **Problem analysis & design:** *An ability to identify analysis /synthesis interprets data to design /develop solutions for complex engineering problem in the field of Electronics and Telecommunication.*
- c. **Professional engineer practice:** *An ability to apply the acquired engineering skills professionally & ethically & understand the impact of engineering solution in social and environmental contexts.*
- d. **Modern tool usage:** *Create, Select and apply appropriate techniques, resources and modern engineering and IT tools, including prediction and modeling to complex engineering activities, with an understanding of the limitation.*
- e. **Communication:** *Communicate effectively on complex engineering activities with the engineering community and with the society at large such as, being able to comprehend and write effective reports and design documentation, make effective presentations, and give and receive clear instructions.*
- f. **Individual and team work:** *Function effectively as an individual and as a member or leader in diverse teams and in multidisciplinary settings.*
- g. **Life-long learning:** *Recognize the need for and have the preparation and ability to engage in independent and lifelong learning in the broadest context of technological change.*
- h. **Industry Orientation:** *Demonstrate knowledge and understanding economic principles, management and telecom regulation.*



THE STAFF FORUM

The EXTC staff department conducted certain workshops, thereby augmenting the departmental mission to help the students emulate paramountcy in confronting the growing industrial challenges...

HDL PROGRAMMING THROUGH FPGA

Duration of workshop: 4 days (05/01/2015 to 08/01/2015)

Credibility of participants: Third year and Final year Engineering

Number of students: 42

(divided into two batches of 21 students each)

Speaker: Mr. Mrugendra Vasmatkar (Asst. Professor EXTC Dept., VESIT)



The purpose of the workshop was to introduce Hardware description language (HDL) concepts and porting of the HDL programs on FPGA along with exhaustive hands on session helping the students to gain confidence in VLSI field. This workshop was definitely encouraging for the students to take up final year projects in VLSI designing and to pursue a career in VLSI domain.

THE ACCESS TO NEURAL NETWORKS



Duration of workshop: 2 days (02/01/2015 to 03/01/2015)

Credibility of participants: Faculty members from various departments and P.G. students

Number of students: 35

Speaker: Dr. Nadir Charniya (Prof. EXTC Dept., VESIT)

The workshop provided insight to the concepts of Neural Networks as a tool for data classification and regression. It also dealt with the design of Neural Networks for various nonlinear complex applications like speech recognition, character recognition, cryptography, pattern recognition, non-linear time series prediction/ interpolation, function approximation, etc. so that the student participants could use it further for their end research.

STUDENT FORUM

*The seniors of EXTC department contributed towards the department's mission through '**peer learning**' by undertaking the responsibility to apprise their juniors with the challenges coming their way...*

The session was indeed a successful event. Our seniors shared their knowledge and experiences with us willingly. They gave us tips and methods of skilful studying, further career options, told us about the preparation required for campus interviews. Then they patiently solved all our doubts, cleared all our queries confidently. Overall it was a wonderful session which made us more aware, also helped us gain a lot of information.

-ZARANA M. (D9C)

We got a valuable opportunity to interact with B.E.(Final year) students. Firstly, two of our seniors, groomed us with what field needs to be resided as an EXTC engineer. Their main motive was to counsel us to take up core jobs into:

- 1) VLSI design
- 2) Embedded system
- 3) Microwave
- 4) Communication systems etc. which have tremendous scope in the future.

They groomed us about the current placement scenario etc.

They also taught us how to enjoy our life in VESIT balancing our academics and extracurricular activities.

-SUKALP K. (D9A)

For me, the interactive session was very engaging. The seniors made a genuine effort to connect with us and our queries, and I believe they were successful in their attempt. They emphasized on the opportunities that will be soon coming our way and how we should seize those opportunities for a brighter future.

-HARIHARR P. (D9B)

The senior interaction was really informative and interesting. All my doubts about post-graduation were clear after they spoke to us. Also the various fields that I could opt for were very clearly put across to me by our seniors. Overall there was a lot to learn from seniors which will really help me in my future.

-POOJA J. (D9A)

Through this short note I would like to thank the seniors for the interactive session. 'All work and no play makes Jack a dull boy' was emphasized properly. I welcome the inspiration from them. The execution and outcome of this session was simply brilliant.

-SHIVESH H. (D9B)



EDITORIAL COUNCIL

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CONGRATULATIONS!!!

SAHIL MEHTA (D9C)

For winning the contest of best newsletter title