



**Vivekanand Education Society  
Institute of Technology**

Hashu Adwani Memorial Complex, Collector's Colony, Chembur, Mumbai, Maharashtra 400074

# SILICON TODAY



January 2024

-

June 2024

**DEPARTMENT  
of  
ELECTRONICS AND COMPUTER SCIENCE**

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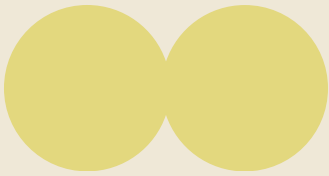
**Creative Minds**

# *Vision*

To produce globally competent and socially responsible engineers capable of independently solving problems with a sound knowledge of engineering in Electronics and Computer Science.

# *Mission*

- To prepare students in the field of engineering in Electronics and Computer Science for lifelong learning.
- To develop professionals who can think critically and provide cost-effective solutions to solve local and global issues using knowledge of Electronics and Computer Science.
- To inculcate a sense of being job providers than job seekers with due consideration to ethical and social values.



# MESSAGE FROM THE DEPARTMENT



MRS. SARIKA KULKARNI

Dear Students,

Albert Schweitzer once said, "Success is not the key to happiness. Happiness is the key to success. If you love what you are doing, you will be successful." As you embark on your academic journey, I urge you to pursue it with unwavering passion and dedication. Engineering is an ever-changing field, full of opportunities for learning and innovation. Let your curiosity and enthusiasm lead you to remarkable achievements.

Our curriculum offers the unique advantage of combining electronics and computer science, giving you a distinct edge and pragmatic skills necessary for future success. This field is not just about circuits and algorithms; it is about transforming ideas into realities that shape the future of technology. Embrace a lifelong love of learning, which will empower you to grow and thrive in every phase of your life.

Today's world values collaboration and teamwork. I encourage you to engage with your professors, seniors, and fellow students to expand your knowledge and develop teamwork skills. Believe wholeheartedly in your abilities, for each of you possesses a unique perspective and potential to contribute to the field. Collaborate with your peers, learning from their insights and sharing your own, as teamwork often leads to breakthroughs that individual effort cannot achieve alone. Remember, necessity leads to invention, and curiosity leads to discovery. Always be curious and ask questions to deepen your understanding. Also, participate in extracurricular activities to improve your skills and build a well-rounded personality.

Let us contribute collaboratively to the field of Electronics and Computer Science, elevating it to new heights of innovation and excellence and shaping the future of technology.

# FACULTY CONTRIBUTION

WORKSHOPS/COURSES/SESSION ATTENDED/  
CONDUCTED/ORGANIZED BY FACULTY

## **1. Dr. Jayamala Adsul**

- Digital India future labs Delhi
- CDAC Bangalore IEP visit to PARAM UTKARSH SUPER COMPUTER
- IEP on "Memory and In-Memory Computing"
- SCI-TECH FAIR '24
- V-REACH 2024
- VLSI conference at KOLKATA

## **2. Mr. Yogesh Pandit**

- SCI-TECH FAIR '24
- SEL LECTURE 2024

## **3. Mrs Naveeta Kant**

- GEN-AI Workshop 2024

# FACULTY CONTRIBUTION

WORKSHOPS/COURSES/SESSION ATTENDED/  
CONDUCTED/ORGANIZED BY FACULTY

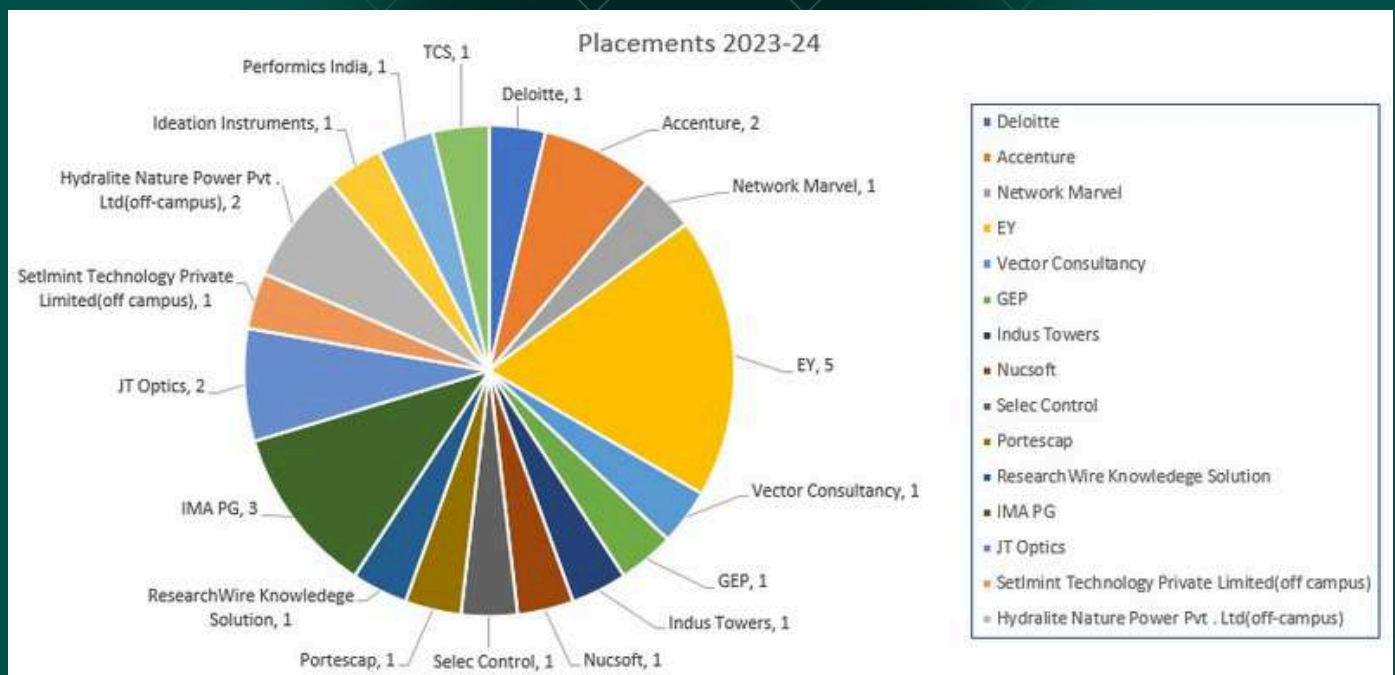
## 4. Dr. Asawari Dudwadkar

- Degree Distribution Ceremony 2024

## 5. Dr. T Rajani Mangala

- Economic Recession Analysis and Prediction - research paper- International Journal of All Research Education and Scientific Methods (IJARESM).
- ISTE Approved One Week STTP On:
- “SecureBloc: A Hands-On Journey into Cybersecurity and Blockchain” from 01 July, 2024–05 July, 2024.
- Final round of Looking Beyond Syllabus (LBS) projects on 11 March 2024.
- BoS member, for Electronics Engineering , for University of Mumbai.
- ISTE approved Self-Financing Faculty Development Program on Gen AI

# PLACEMENTS



Total **43** companies visited last year and out of **60** eligible and interested student **26** students were placed. Percentage of placed students is about **44%**.

# PLACEMENT ACTIVITIES

1. Three days Workshop on “Barclays Life skills\_ Employability Skills: Preparing for Interviews/ Mock Interviews” Organized by Training and Placement Cell.



2. Mini Project - A Stepping Stone.



# PLACEMENT ACTIVITIES

## 3. Alumni Interaction - Mr. Tanmay Kothale



## 4. Steering the Interview



# ALUMNI INTERACTION

## V-REACH'24

**Date of Event:** 23/04/2024

**Name of Speaker:** Mr. Anish Jha

**Name of Faculty:** Dr. Jayamala Adsul

**Name of Interviewer:** Siddharth Suyal

### Objectives:

- **Provide a platform focused on the Fundamentals of Power Electronics, supporting outcome-based education.**
- **Enable knowledge and expertise sharing of alumni for the professional development of students.**
- **Engage stakeholders to broaden VESIT's outreach.**
- **Foster effective affiliation with alumni, leveraging their expertise for the benefit of VESIT and its students.**



# PROJECT COMPETITION

## LBS I2I 2024

**Date of Event: 11/03/2024 & 21/03/2024**

**Name of Faculty Co-ordinator:**

- **Dr (Mrs) T. Rajani Mangala, LBS Incharge**
- **Mrs. Priya R. L, LBS Coordinator, CMPN**
- **Mrs. Ruchi Rautela, LBS Coordinator, MCA**
- **Mrs. Jayshree Hajgude: LBS Coordinator, INFT**
- **Mr. Ajinkya Valanjoo: LBS Coordinator, AI & DS**
- **Mrs. Anuradha Jadiya: LBS Coordinator, EXTC**
- **Mrs. Gauri Sahoo, LBS Coordinator, ETRX**
- **Dr. Kadambari Sharma: LBS Coordinator, INST**

**Objectives:** Opportunity to the students to contribute to the society by providing innovative and efficient solutions for today's needs of the citizens. Three best projects were declared as winners among the 14 participating teams. In the finals 7 teams demonstrated their completed projects. The projects were judged based on the evaluation and feedback received by the judges.



# RESEARCH GRANT

## FPGA Boards Granted

### -Annexure Deliverables-

-The items (hardware and software) have been dispatched to you under the C2S program. Deliverables details according to the attached annexure.

**-10 Lakh total cost**

-FPGA boards

Vivado + Vitis 50 licence

## Intel FPGA Academic Program

The email from the Intel® FPGA Academic Program confirms the approval of a license request for Intel® Quartus® Prime Pro Edition Software, granting two licenses. It provides instructions to generate the license file by logging into the Self Service Licensing Center and includes links to helpful resources for installation, licensing, and technical support. The email is addressed to Jayamala Adsul and concludes with contact information for further assistance.



# VLSI WORKSHOP

## Description

This program offers hands-on training in VLSI design using Cadence Virtuoso. Students will learn MOSFET characteristics, CMOS inverter design, and logic gate simulations, while working on practical projects such as amplifiers, multiplexer circuits, and PLLs. By the end, participants will gain essential skills and experience for careers in VLSI design.



# INDUSTRIAL VISITS



**Venue:** IMA-PG, Navi Mumbai

**Date of the Event:** 02/02/2024

**Name and details of the Convener (society in charge faculty):** Dr. Rajani Mangala & Dr. Asawari  
Dudwadkar

**Name and details of the Coordinator (students in organising team):** Ms. Ashiqa & Ms. Jheel

## **Objective Of The Program:**

The objective of the industrial visit to IMA-PG India Private Limited is to provide students with practical exposure to pharmaceutical packaging machinery and processes, enhance their understanding of industry practices and innovations, facilitate interactive learning through sessions and Q&A opportunities, promote professional networking with industry experts, and align the visit with the academic curriculum to complement theoretical knowledge with hands-on experience.



# SEL LECTURE 24



**Date of the Event:** 01/04/2024

**Name of Speaker:**  
**Ms. Archita Malgaonkar**

**Name and details of the Coordinator :**  
**Mr. Yogesh Pandit**

## **Objective Of The Program:**

The Electronics and Computer Science Department of Vivekanand Education Society's Institute of Technology organized an SEL Lecture on "Scope/Opportunities of VLSI in INDIA" for TE students on April 1st, 2024. The lecture offered valuable insights into VLSI trends, opportunities in academia and industry, and the current landscape of VLSI applications. The session also addressed future challenges in VLSI and Semiconductor Technology, with active participation during the Q&A session fostering meaningful discussion and knowledge exchange.



# SCI-TECH FAIR 2024



**Venue: Lab No 201 and 202**

**Date of the Event: 02/02/2024**

**Faculty members coordinated the “SCI TECH FAIR ‘24 ”: Dr. Jaymala Adsul, Mr. Yogesh Pandit**

## **Objective Of The Program:**

The project exhibition started at 11 AM. The response was very good from participants. The projects were exhibited for Staff and Students from VESIT and had healthy discussions with them. HOD and Dy.HOD visited all the project groups and encouraged them. Participating project groups explained the design process and implementation challenges faced during the course of the project. The correct approaches and design solutions were discussed. Around 50+ students presented 10 Projects during the exhibition.



# STUDENT'S CORNER

## INTERNSHIPS

- **Shardul Mangesh Kadam(D11)** - Bhabha Atomic Research Centre (B.A.R.C)
- **Aditya Sawant(D11)** - Bhabha Atomic Research Centre (B.A.R.C)
- **Vineet Bhagwan Wagh (D6)** - Edunet Foundation
- **Srushti Chopade (D6)** - Edunet Foundation
- **Joanna Sanju (D6)** - Al Jaber Energy Services, Abu Dhabi, UAE
- **Prajwal Kudapane (D6)**- Automotive Electronics
- **Shreya Sakpal (D6)** - InternForte
- **Shardul Mehendale (D1EC)** - ISA workshop
- **Mitalee Shirsat** - Lemion Global

# STUDENT'S CORNER

## INTERNSHIPS

- **Piyush Arvind Karbhajan** - Rising Education
- **Parth Chavan (D11EC)** - Sporko
- **Maitrey Khedekar** - Prodigy Infotech
- **Ayush Pramod Naik** - Prodigy Infotech
- **Khushi Sharma (D6)** - Hiremi
- **Atharva Jitendra Shinde** - Tata Institute of Fundamental Research
- **Omprakash Subhash Dhawale** - IMA-PG pvt ltd
- **Vansh Hitesh Dhoka (D11)** - IMA-PG India Pvt. Ltd.
- **Ratan Poojari** - Snapmagic Technologies LLP

# STUDENT'S ACHIEVEMENTS

INVICTUS 24

**ACHIEVEMENT: WINNER**

Team :- Harsh Kapse, Atharva Phadtare, Binayak Bhattacharjee, Vinayak Panchal



D6

TECH-A-THON 24 (48 HOUR HACKATHON)

**ACHIEVEMENT: 2nd Runner-up**

Team :- Harsh Kapse, Atharva Phadtare



D6

SYRUS'24 HACKATHON

**ACHIEVEMENT: 2ND ROUND**

Team :- Harsh Kapse, Atharva Phadtare, Binayak Bhattacharjee, Vinayak Panchal



D6

# STUDENT'S ACHIEVEMENTS

UTSAV 24

EVENT NAME: MONO DRAMA  
ORGANISED BY: CULTURAL COUNCIL VESIT  
ACHIEVEMENT: RUNNER UP

Gurkeerat Singh (D11)



UTSAV 24

EVENT NAME: MONO DRAMA  
ORGANISED BY: CULTURAL COUNCIL VESIT  
ACHIEVEMENT: RUNNER UP

Kamya Gupta (D1EC)



UTSAV 24

EVENT NAME: MANDALA  
ORGANISED BY: CULTURAL COUNCIL VESIT  
ACHIEVEMENT: WINNER

Shruti Thakare (D1EC)

# STUDENT'S ACHIEVEMENTS

UTSAV 24

EVENTS : INVESI TENDER

ORGANISED BY: CULTURAL COUNCIL VESIT

ACHIEVEMENT: RUNNER UP

Mansi Mohite, Purvi Prasad ( D 6 )



SPHURTI 24

EVENT NAME: SENIOR SPHURTI'23 TUG OF WAR

ORGANISED BY: SPORTS COUNCIL VESIT

ACHIEVEMENT: WINNER

D16

SPHURTI 24

EVENT NAME: SENIOR SPHURTI'23 ATHLETICS

ORGANISED BY: SPORTS COUNCIL VESIT

ACHIEVEMENT: WINNER

Om Lokegaonkar



# STUDENT'S ACHIEVEMENTS

SPHURTI 24

EVENT NAME: SENIOR SPHURTI'24 TUG OF WAR  
ORGANISED BY: SPORTS COUNCIL VESIT  
ACHIEVEMENT: WINNER



D6 GIRLS

SPHURTI 24

EVENT NAME: SENIOR SPHURTI'23 HANDBALL  
ORGANISED BY: SPORTS COUNCIL VESIT  
ACHIEVEMENT: RUNNER-UP

D6

SPHURTI 24

EVENT NAME: SENIOR SPHURTI'23 ATHLETICS  
ORGANISED BY: SPORTS COUNCIL VESIT  
ACHIEVEMENT: WINNER



Harshita Anchan (D6)

# STUDENT'S ACHIEVEMENTS

ALGERIA 24

EVENT NAME: SKIT COMPETITION

ORGANISED BY: PILLAI GROUP OF INSTITUTION

ACHIEVEMENT: 3RD PRIZE

Arnav Nadkarni, Kalpesh Rathod



JUNIOR SPHURTI 24

EVENT NAME: JUNIOR SPHURTI'24 FUTSAL

ORGANISED BY: SPORTS COUNCIL VESIT

ACHIEVEMENT: WINNER

D11EC



Girls Futsal Winners-D11EC

LBS 24

EVENT NAME: LBS 2024

ORGANISED BY:

ACHIEVEMENT: WINNER

Sahil Sawant, Kapil Dhavale, Shreyash Das (D6)



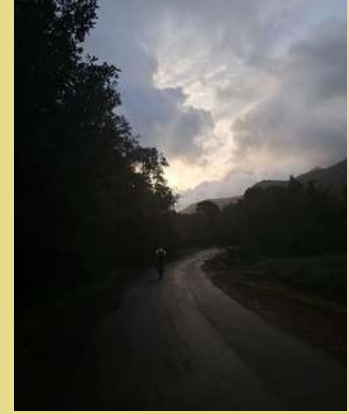
# CREATIVE MINDS



Anushka Shinde (D6)



Anushka Shinde (D6)



Kapil Dhavale (D6)



Sujay Thasale (D6)



Mansi Mohite (D6)



Joanna Sanju (D6)



Joanna Sanju (D6)



Kapil Dhavale (D6)



Kapil Dhavale (D6)

## Faculty In Charge



**HEAD OF DEPARTMENT**  
Mrs. Kavita Tewari



**ASSISTANT PROFESSOR**  
Mrs. Jayamala Adsul

## Student In Charge



**JR. EDITOR**  
Harsh Kapse



**JR. EDITOR**  
Joanna Sanju



**JR. EDITOR**  
Ritali Jadhav