

1	2	3	4	5	6
Siddharth Rawlani	Medical Image Analysis for Early diagnosis of diseases.	His	performance during internship was	Excellent.	Amrita Jhaveri
Kesar Jotwani	Medical Image Analysis for Early diagnosis of diseases.	Her	performance during internship was	Good.	Amrita Jhaveri
Manali Bhawe	Medical Image Analysis for Early diagnosis of diseases.	Her	performance during internship was	Excellent.	Amrita Jhaveri
Doulat Lalwani	Medical Image Analysis for Early diagnosis of diseases.	His	performance during internship was	Excellent.	Amrita Jhaveri
Kanak Pendse	Medical Image Analysis for Early diagnosis of diseases.	Her	performance during internship was	Excellent.	Amrita Jhaveri
Priyosh Mondal	Medical Image Analysis for Early diagnosis of diseases.	His	performance during internship was	Excellent.	Amrita Jhaveri
Arya Kurup	Medical Image Analysis for Early diagnosis of diseases.	Her	performance during internship was	Excellent.	Amrita Jhaveri
Om Gaydhane	Medical Image Analysis for Early diagnosis of diseases.	His	performance during internship was	Excellent.	Amrita Jhaveri
Harshita Anala	Medical Image Analysis for Early diagnosis of diseases.	Her	performance during internship was	Excellent.	Amrita Jhaveri
Gaurang Mapuskar	VLSI implementation of DSP algorithm using MATLAB simulink	His	performance during internship was	Satisfactory.	Dr. Asma Parveen Siddavatam
Shubham Kumar	IoT and AI based project	His	performance during internship was	Fair.	Dr. Anjali Yeole
Aryan Chaudhary	IoT and AI based project	His	performance during internship was	Fair.	Dr. Anjali Yeole
Tejas Sharma	Image Processing and AI based project	His	performance during internship was	Fair.	Dr. Anjali Yeole
Shravani Pore	IoT and AI based project	Her	performance during internship was	Fair.	Dr. Anjali Yeole
Priyanshu Singh	Image Processing and AI based project	His	performance during internship was	Fair.	Dr. Anjali Yeole
Sneha Kadambala	Image Processing and AI based project	Her	performance during internship was	Fair.	Dr. Anjali Yeole
NIKHIL HASWANI	CPULSER PVT LTD.	His	performance during internship was	Fair.	Dr. Asma Parveen Siddavatam
ADITYA BAGAYATKAR	VLSI implementation of DSP algorithm using MATLAB simulink	His	performance during internship was	Good.	Dr. Asma Parveen Siddavatam
Dinesh Ubrani	VLSI implementation of DSP algorithm using MATLAB simulink	His	performance during internship was	Good.	Dr. Asma Parveen Siddavatam
Prabha Pamula	VLSI implementation of DSP algorithm using MATLAB simulink	His	performance during internship was	Good.	Dr. Asma Parveen Siddavatam
Drishti Samvedi	VLSI implementation of DSP algorithm using MATLAB simulink	His	performance during internship was	Good.	Dr. Asma Parveen Siddavatam

Diya Shah	web developement CPULSER PVT LTD	His	performance during internship was	Good.	Dr. Asma Parveen Siddavatam
Hritika Mulay	Design of analog comparator	Her	performance during internship was	Satisfactory.	Dr. Jayamala Adsul
Ashiqa Shervegar	FPGA based wifi module	Her	performance during internship was	Satisfactory.	Dr. Jayamala Adsul
Jheel Panchal	FPGA based wifi module	Her	performance during internship was	Satisfactory.	Dr. Jayamala Adsul
Sanket Kapoor	FPGA based wifi module	His	performance during internship was	Satisfactory.	Dr. Jayamala Adsul
SAGAR PANDEY	FPGA based wifi module	His	performance during internship was	Satisfactory.	Dr. Jayamala Adsul
Udit Koli	comparator	His	performance during internship was	Satisfactory.	Dr. Jayamala Adsul
Sharvari Patwardhan	comparator	Her	performance during internship was	Satisfactory.	Dr. Jayamala Adsul
Pradeesh Reddy	comparator	His	performance during internship was	Satisfactory.	Dr. Jayamala Adsul
Radhika Damle	VLSI implementation of DSP algorithm using MATLAB simulink	Her	performance during internship was	Excellent.	Mrugendra Vasmatkar
SHREYAS SARANG	VLSI implementation of DSP algorithm using MATLAB simulink	His	performance during internship was	Excellent.	Mrugendra Vasmatkar
Pooja Ghadage	VLSI implementation of DSP algorithm using MATLAB simulink	Her	performance during internship was	Excellent.	Mrugendra Vasmatkar
Milind Sawant	Prototyping if FPGA using VERILOG Programming	His	performance during internship was	Good.	Mrugendra Vasmatkar
Shantanu Deshmukh	Prototyping if FPGA using VERILOG Programming	His	performance during internship was	Excellent.	Mrugendra Vasmatkar
Kaustubh Pawar	Prototyping if FPGA using VERILOG Programming	His	performance during internship was	Good.	Mrugendra Vasmatkar
Alrich Xavier Dsouza	Prototyping if FPGA using VERILOG Programming	His	performance during internship was	Excellent.	Mrugendra Vasmatkar
AAYUSHI SANTOSH RAJESHIRKE RAJESHIRKE	Prototyping if FPGA using VERILOG Programming	Her	performance during internship was	Good.	Mrugendra Vasmatkar
Pratik Patkar	Prototyping if FPGA using VERILOG Programming	His	performance during internship was	Excellent.	Mrugendra Vasmatkar
Atharva Petkar	Prototyping if FPGA using VERILOG Programming	His	performance during internship was	Excellent.	Mrugendra Vasmatkar
Tanmayee Bochare	Prototyping if FPGA using VERILOG Programming	Her	performance during internship was	Good.	Mrugendra Vasmatkar
Pankaj Ukirde	Protective Relay (Protech Engg)	His	performance during internship was	Satisfactory.	N. Gopalakrishnan
Shubham Nayak	Protective Relay (Protech Engg)	His	performance during internship was	Satisfactory.	N. Gopalakrishnan
Prathamesh Komte	IOT based Power Meter	His	performance during internship was	Fair.	N. Gopalakrishnan
Nilesh Sharma	Protective Relay (Protech Engg)	His	performance during internship was	Fair.	N. Gopalakrishnan

Sanchit Mhadgut

MIHIR SUHANDA

Avantika Narvekar

Vaibhav Singh

Protective Relay (Protech Engg)	His	performance during internship was	Excellent.
Protective Relay (Protech Engg)	His	performance during internship was	Satisfactory.
Protective Relay (Protech Engg)	Her	performance during internship was	Satisfactory.
IOT based Power Meter	His	performance during internship was	Fair.

N. Gopalakrishnan
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