

V.E.S INSTITUTE OF TECHNOLOGY

EXAMINATION TIME TABLE (DECEMBER 2020)

PROGRAMME - T.E. (Electronics) (REV.-2012)(CBSSG)

SEMESTER - V

Days and Dates	Time	Course Code	Paper
Wednesday, December 2, 2020	4.30 p.m to 5.30 p.m	EXCS01	MICROCONTROLLERS AND APPLICATIONS
Friday, December 4, 2020	4.30 p.m to 5.30 p.m	EXCS02	DESIGN WITH LINEAR INTEGRATED CIRCUITS
Monday, December 7,2020	4.30 p.m to 5.30 p.m	EXCS03	ELECTROMAGNETIC ENGINEERING
Wednesday, December 9, 2020	4.30 p.m to 5.30 p.m	EXCS04	SIGNALS & SYSTEMS
Friday, December 11, 2020	4.30 p.m to 5.30 p.m	EXCS05	DIGITAL COMMUNICATION

Important Note: ● Change if any, in the time table shall be communicated on the college web site.

Mumbai

10th November 2020



PRINCIPAL

PRINCIPAL,
VIVEKANAND EDUCATION SOCIETY'S
INSTITUTE OF TECHNOLOGY
MASHU AVALDI, 4, JARWAL COMPLEX,
COLLECTOR'S CHISTRY CHEMBUR,
MUMBAI-400 071, INDIA